

| Class | Subclass |
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**ISSUE CLASSIFICATION**

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PATENT NUMBER

U.S. ~~UTILITY~~ Patent Application

**O.I.P.E.**

PATENT DATE

SCANNED

**Q.A.**                     

|                              |                   |              |                 |                                    |                      |
|------------------------------|-------------------|--------------|-----------------|------------------------------------|----------------------|
| APPLICATION NO.<br>09/500607 | CONT/PRIOR<br>YES | CLASS<br>375 | SUBCLASS<br>375 | ART UNIT<br><del>375</del><br>2631 | EXAMINER<br>D. J. H. |
|------------------------------|-------------------|--------------|-----------------|------------------------------------|----------------------|

## APPLICANTS

Robert Koudelka

# TITI E

Wide bandwidth phase-locked loop circuit

PTO-2040  
12/99[illegible]

|   |  |             |                                   |              |
|---|--|-------------|-----------------------------------|--------------|
| <input type="checkbox"/> <b>TERMINAL<br/>DISCLAIMER</b>   | <b>DRAWINGS</b>                              |             | <b>CLAIMS ALLOWED</b>             |              |
|   | Sheets Drwg.                                 | Figs. Drwg. | Print Fig.                        | Total Claims |
| <input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.  | _____<br>(Assistant Examiner) (Date)         |             | <b>NOTICE OF ALLOWANCE MAILED</b> |              |
|   | _____<br>(Primary Examiner) (Date)           |             | <b>ISSUE FEE</b>                  |              |
| Amount Due  |  |             | Date Paid                         |              |
| <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____<br>_____<br>_____ | _____<br>(Legal Instruments Examiner) (Date) |             | <b>ISSUE BATCH NUMBER</b>         |              |
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